## SEARCHED

| Class | Sub. | Date | Exmr. |
|---|---|---|---|
| 712 | 23 | 06/10/04 | lc |
| | 228 | | |
| | 248 | | |
| 711 | <150 | | |
| 718 | <108 | | |

## SEARCH NOTES
(INCLUDING SEARCH STRATEGY)

| | Date | Exmr. |
|---|---|---|
| USPAT | 06/10/04 | lc |
| PGPub Full-Text | | |
| JPOABS | | |
| EPOABS | | |
| DERWENT | | |
| IBM T.D.B | | |
| Host Emulated processor with the loading mechanism Searched | | |
| Thread switching and synchronization Searched | | |

## INTERFERENCE SEARCHED

| Class | Sub. | Date | Exmr. |
|---|---|---|---|
| | | | |

(RIGHT OUTSIDE)

BEST AVAILABLE COPY